

St. Xavier's Catholic College of Engineering, Chunkankadai, Nagercoil – 629003

(An Autonomous Institution)

Department of Electronics and Communication Engineering

Board of Studies Meeting

Date : 13/12/2025

Venue : Board Room

MEMBERS PRESENT

Sl. No.	Name & Designation	Institution	Signature
1.	Dr. N.B. BALAMURUGAN, PROFESSOR	TCE, MADURAI	
2.	Dr. MALAYA KUMAR NATH ASST. PROF	NIT PUDUCHERRY KARAIKAL	
3.	Ancu John.M	CDAC, TUM	
4.	Edwin Lawrence S	Tessdva Semi P.V.L.D.	
5.	Dr. J. MAHESWARAN, PRINCIPAL	EP SXCE	
6.	Dr. R.P. ANTO Kumar, Dean (Acad.)	SXCC12	
7.	Dr. V. Christos Jeya Singh Vice Principal	SXCCE	
8.	Dr. T. Mary Littleflower / AP	SXCCE	
9.	A. Anitha / AP	SXCCE	
10.	V. Femila Sano / AP	SXCCE	
11.	Dr. B.C. Preethi, Asso. Prof.	SXCCE	
12.	Dr. S. Abha / Asso. Prof.	SXCCE	
13.	K. Baby Lisa / AP	SXCCE	
14.	Dr. A. MILTON, Prof.	SXCCE	
15.	Dr. L. Femila, Asso. Prof.	SXCCE	
16.	Flavin Raja Singh R / AP	SXCCE	

17.	A.C. Jinisha / AP / ECE	SXCCE	<u>Ad</u>
18.	D. Jeraldin Auxillia / P / ECE	SXCCE	<u>As</u>
19.	T. LATHA / P / ECE	SXCCE	<u>Asli</u>
20.	S. Mary Vasanthi / Asso Prof	SXCCE	<u>Asyl</u>
21.	C. Sheeja Herath, Rani / Asso. Prof / ECE	SXCCE	<u>Aji</u>
22.	Maceal Tony / L / AP / ECE	SXCCE	<u>ij</u>
23.	Dr. S. Maria Scrophin Saji Tho	SXCCE	<u>ij</u>
24.	Dr. Y.R. Annie Besant	SXCCE	<u>As</u>
25.	Dr. S. CAROLINE / HOD	SXCCE	<u>As</u>
26.	Dr. C. SELDEV CHRISTOPHER P / ECE	SXCCE	<u>As</u>
27.	Dr. e. Helen Sulechana P / ECE	SXCCE	<u>ij</u>
28.	C. Renit / AP	SXCCE	<u>ij</u>
29.	T. Ajitha / Asst. Prof	SXCCE	<u>As</u>
30.	Dr. M. STARWIN, AP / ECE	SXCCE	<u>As</u>
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33.			
34.			
35.			



St. XAVIER'S
CATHOLIC COLLEGE OF ENGINEERING
(Autonomous)

Chembur, Nagarcovil - 629003
Kanyakumari District, Tamil Nadu

Approved by AICTE & Affiliated to Anna University, Chennai
Accredited with 'A' Grade by NAAC
UG Programs(ECE, EEE, Mech, Civil, CSE & IT); Accredited by NBA
Anna University Recognized Research Institute
Recognized under section 2(f) & 12(B) of UGC Act, 1956
UG Programs(ECE, EEE, Mech, Civil, CSE & IT);
MBA & MCA Programs Permanently Affiliated

Department of Electronics and Communication Engineering

Board of Studies Meeting Minutes # 07

Date : 13/12/2025

Time : 10.30 am – 1.00 pm

Venue : Hybrid Mode (Board Room)

The seventh Board of Studies (BoS) meeting for the Department of ECE was held on December 13, 2025, at 10:30 a.m. in the Board Room. All faculty members of the department attended, joined by an Anna University nominee, two Subject experts, an industry representative, and a distinguished alumnus. The meeting commenced at 10.30 a.m. with a prayer by Mrs. A. Anitha AP/ECE. Mrs. V. Femila Savio, AP/ECE, warmly welcomed the members. Dr. S. Caroline, Head of the Department of ECE, presented the detailed Curriculum (I to VIII semesters) and Syllabus for first and second semester UG programme for R2026. Dr.T.Latha, co-ordinator of M.E. Communication Systems outlined the curriculum and syllabi for the first semester of the M.E Communication Systems programme (R2026). Dr. C. Helen Sulochana, co-ordinator of M.E. Medical Electronics outlined the curriculum and syllabi for the first semester of the M. E Medical Electronics programme (R2026), Dr.Y.R.Annie Bessant, co-ordinator of M.E. VLSI Design outlined the curriculum and syllabi for the first semester of the M.E VLSI Design programme (R2026). All the BoS members offered their valuable suggestions, and the meeting concluded with a vote of thanks delivered by our Principal Dr. J.Maheswaran.

University Nominee for BoS

1. Dr.N.B.Balamurugan, Professor, Department of ECE, Thiagarajar College of Engineering, Madurai.

Expert Members in the Subject from outside the college

1. Dr. D. Jude Hemanth, Associate Professor/ ECE Karunya Institute of Technology & Sciences, Coimbatore.
2. Dr. Malaya Kumar Nath, Assistant Professor/ ECE, NIT Puducherry, Thiruvettakudy, Karaikal, Puducherry.

Representative from Industry

1. Mr.S.Edwin Lawrence, Design Lead - DFT, Tessolve Semiconductors Pvt.Ltd., Whitefield Office, Bangalore.

Postgraduate Meritorius Alumnus

1. Mr. Ancil John M, Project Engineer, Centre for Development of Advanced Computing, Thiruvananthapuram.

Agenda for the Meeting

- 07.01. Confirmation of the Sixth BoS meeting minutes held on 22.05.2025 and Decision/Action Taken report.
- 07.02. Discussion on the Suggestions / Recommendations offered by the members in the Sixth Academic Council meeting and the Sixth Governing Body meeting.
- 07.03. Presentation of the proposed curriculum and syllabi and getting recommendation for approval for the Draft Curriculum and Syllabi of Semester I UG and PG Programmes for regulation 2026.
- 07.04. Other matters, if any.

Discussion

- 07.01. Confirmation of the Sixth BoS meeting minutes held on 22.05.2025 and Decision/Action Taken report.

Sl. No.	Suggestions / Recommendations / Related to Curriculum and Draft syllabi by the members during VI BoS Meeting	Action Taken	Suggestions / Recommendations / Remarks by the members during VII BoS Meeting
EC22801 / PROJECT WORK/ INTERNSHIP			
01	It was suggested to group the entire project batches into different domains (VLSI, Communication, Embedded, IoT, Image Processing etc...). For evaluating Project review, form Committees based on the faculty domains and involve all faculty in evaluation.	Will be considered in future.	Confirmed.
Curriculum and Syllabi of M.E. VLSI Design			
02	It was suggested to include Graph Theory and Optimization Techniques course if it is available in any other University's VLSI Design syllabus. Otherwise, Probability and Random Process can be included instead of Graph Theory.	In VLSI design simulation and synthesis aspects, it is mandatory to include Graph Theory and Optimization Techniques.	Confirmed.
03	It was suggested to include the Professional Elective course (Semester I), Semiconductor Devices and Modeling as a core in the first semester.	Implemented.	Confirmed and approved.
04	It was suggested to include "Pattern Recognition and Computational Intelligence using MATLAB" book as reference in the Pattern Recognition course (VL22116) [SEMESTER II, PE - I]	Considered and implemented	Confirmed and approved.
05	It was suggested to replace the course Data Converters (VL22211) with Mixed Signal VLSI Design. [SEMESTER II, PE II]	Considered and confirmed.	Confirmed and approved.
06	It was suggested to replace the course MEMS and NEMS (VL22224) with CAD for VLSI Design	Implemented.	Confirmed and approved.

	It was suggested to replace the course Soft Computing and Optimization Techniques (VL22115) with AI related course/ Hardware Security course	Confirmed with Hardware Security	Confirmed and approved.
08	The courses DSP Structures for VLSI (VL22221) [PE III], VLSI Signal Processing (VL22323) [PE V] and VLSI Architectures for Image Processing (VL22311) [PE IV] may have similar contents	DSP Structures for VLSI (VL22221) is removed as it has similar contents in VLSI Signal Processing. VLSI Architectures for Image Processing (VL22311) is replaced with the course Physical Design Automation.	Confirmed and approved. Confirmed and approved.
09	Instead of Machine learning (VL22314) add Deep learning and AI concepts, ML can be added in Pattern Recognition.	Machine Learning is replaced by Deep learning. Machine learning concepts will be included in Unit IV and V of Pattern Recognition [SEMESTER II, PE – I]	Confirmed and approved.
10	It was suggested to include basic contents of Image processing in one of these two courses VL22311 – VLSI Architectures for Image Processing or VL22313 – Digital Imaging and Video Processing.	VL22311 – VLSI Architectures for Image processing is removed from curriculum and replaced with the course Physical Design Automation. In VL22313 – Digital Imaging and Video Processing, some basic contents of Image processing will be included [Semester III, PE IV]	Confirmed and approved.

07.02. Discussion on the Suggestions / Recommendations offered by the members in the Sixth Academic Council meeting and the Sixth Governing Body meeting.

There were NO Programme Specific Suggestions / Recommendations offered by the members in the Sixth Academic Council and the Sixth Governing Body meeting.

07.03. Presentation of the proposed Curriculum and Syllabi and getting recommendation for approval for the Draft Curriculum and Syllabi of Semester I UG and PG Programmes for regulation 2026.

Sl. No	Suggestions /Recommendations/ Remarks by the members during VII BoS Meeting	Action Taken
Curriculum and Syllabi of B.E. ECE Semester I		
1	It was suggested to change the title of the course “EC26101 - Basic Electrical and Electronics Engineering” as “Basic Electronics Engineering”, and include some basic Electronics concepts in Unit 1, change Unit 3 title as Semiconductor Physics after referring the syllabi of Physics for Electronics Engineering, and instead of the Electrical concepts in Unit 3 and Unit 4 include Electronics concepts.	Changes are made as suggested
2	It was suggested to change the L T P of the Course EC26201 - Electronic Devices Theory and Characteristics as 30 0 30.	Implemented

3	It was suggested to change the title of the course "EC26202 – Electrical Circuits and Network theorems" "Passive Electronic Circuits and Network Theorems"	Implemented
4	It was suggested to change the Course code of "IT2620 – Data Structures using C" with CS code and faculty from ECE can handle the course.	Implemented
5	It was suggested to change the title of the course "EC26305 – Circuits Laboratory" as Electronics Circuits Laboratory.	Implemented
6	In Vertical 4, it was suggested to change the course title "Advanced Deep Learning Architecture" as Deep Learning Architecture.	Implemented
7	It was suggested to add Quantum Computing course instead of Quantum Communication in Vertical 6.	Implemented
8	It was suggested to add Verilog Concept as one unit in the course "EC26301 - Digital Logic Design" while framing the syllabus.	Will be considered while framing the syllabus.
9	In Industry Collaborated Courses, it was suggested to add LabVIEW based course and in the course Unity 3D add some basics of VR.	Will be considered
10	In Open Elective course list, it was suggested to add one Basic Electronics course.	Included the course "Electronics Fundamentals"
Curriculum and Syllabi of M.E. Communication Systems		
11	It was suggested to change the title of "CU26103 - Advanced Digital Signal Processing" as Digital Signal Processing, Change the name Mini project and add only experiments. Change the Unit 5 title as "DSP Applications", add some applications related with DSP and add Quadrature Mirror Filter in 5 th unit.	Since in UG programme, there is a course "Digital Signal Processing", we retained "Advanced Digital Signal Processing" for M. E Communication Systems
12	It was suggested to avoid using course titles that begin with words such as Basics, Advanced or Introduction.	Implemented
13	It was suggested to change the title of the course "CU26202 Machine Learning" as Machine Learning for Communication System or Deep Learning.	The course "Machine Learning" is changed to "Deep Learning"
14	It was suggested that CO and PO mapping shall be done correctly in accordance with Outcome-Based Education guidelines.	Done accordingly
15	It was suggested to frame Course Outcomes (COs) at higher cognitive levels in accordance with Outcome-Based Education guidelines, specifically for the course CU26103 - Advanced Digital Signal Processing.	COs were framed as suggested
16	In Elective courses, it was suggested to add some courses like 5G Communication, IoT, Antenna Design.	Two courses are added in Elective 1)5G Communication Networks

		Networks 2) Artificial Intelligence and Internet of Things
Curriculum and Syllabi of M.E. Medical Electronics		
17	It was suggested that CO and PO mapping shall be done correctly in accordance with Outcome-Based Education guidelines	Done accordingly
Curriculum and Syllabi of M.E. VLSI Design		
18	It was suggested to replace either the course "VL26201-Multicore Architecture and ISA Verification" or an Elective with the course Semiconductor Device Modelling.	The course VL26201-Multicore Architecture and ISA Verification" is replaced with Semiconductor Device Modelling.
19	It was suggested to add more EDA tools in the theory part and include more practical using EDA tools.	Introduced TCP courses where the suggestions will be incorporated



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[Signature]
Prof. P. ANEESHIMAR, M.E., Ph.D.,
 Dean Academics
 Dean (Academics)
 Member Secretary (Academic Council)
 St. Xavier's Catholic College of Engineering
 Chunkankadai, Nagercoil - 629 003.

[Signature]
 Principal

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 13/12/2025

Dr.N.B.Balamurugan
 (University Nominee)

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Dr. D. Jude Hemanth
 (Subject Expert)

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Dr. Malaya Kumar Nath
 (Subject Expert)

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Mr.S.Edwin Lawrence
 (Industry Representative)

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Mr. M. Ancil John
 (Meritorious Alumnus)